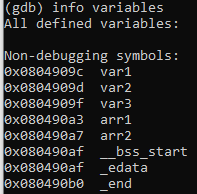


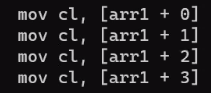
Check p.23 of intro to x86



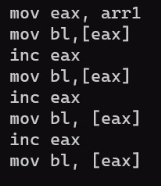
The actually variables



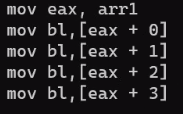
Displacement, doesn’t change the value, rather only traverse through the array



This also works, I guess.

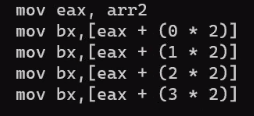


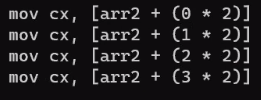
Or this I guess



They are just different, indirect, direct immediate thingy, not sure.

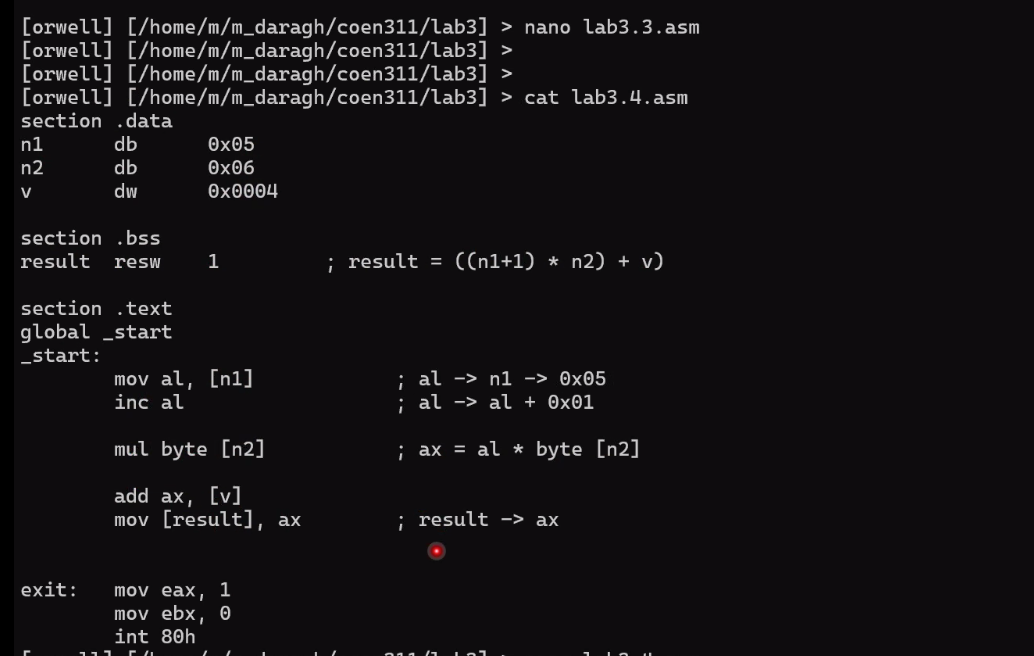
Nasm has smth called “Scaling” used to accommodate the size of the element





Check more intel addressing modes 1st “page”

GUIDED SOLUTION

-debugging with details